Effect of the Inner Current Loop on the Voltage Regulation for Three-Phase Photovoltaic Inverters

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Abstract—In three-phase grid-connected PV inverters, regulating the input voltage is a fundamental requirement. In order to reduce the influence of the PV non-linear behavior and ensure stability in the whole operating range, the input capacitance is currently oversized. This paper reveals the important effect of the inner current loop in the voltage stability and proposes to use a Proportional (P) controller instead of a PI controller. If tuned following the guidelines provided in this paper, the P controller makes it possible to design a stable voltage loop without increasing the input capacitance, thus reducing the converter cost.

Keywords—control design, control stability, inner current loop, photovoltaic generator, three-phase inverter.

I. INTRODUCTION

In order to connect the photovoltaic (PV) generator to the grid, three-phase inverters with an LCL filter are frequently used, as shown in Fig. 1 [1]. With the purpose of controlling the PV generator operating point in this system, an input voltage regulation is carried out by means of a cascaded regulation, where the outer loop obtains the active current reference to be injected into the grid. The control scheme is shown in Fig. 1 for the particular case of a current control in dq reference frame with no feedforward compensations, where the superscript * represents reference variable and the subscript f represents measured and filtered variables. As can be observed, the d current is used to control the PV voltage while the q current makes it possible to set the reactive power injected into the grid.

The voltage regulation must be designed to remain stable in the whole operating range. However, it has been shown that the open-loop transfer function of the dc voltage regulation strongly depends on the operating point, and a Right-Half-Plane (RHP) pole appears when the grid voltage is below the Maximum Power Point (MPP) voltage [2], [3]. Although this operating point is not sought, the system can end up in this situation after an irradiance drop or a missed MPPT algorithm. In this case, in order to ensure stability, the crossover frequency should be set at least two times higher than the frequency of the RHP pole [4]. However, to reject the second harmonic in the dc voltage inherent to unbalanced grids (especially significant during asymmetric voltage sags), the crossover frequency is limited to 10–15 Hz. For this reason, the input capacitor must often be oversized in order to reduce the RHP pole frequency and then guarantee voltage control stability [5].

To avoid oversizing the capacitor, it has been proposed to modify the voltage control so that a virtual impedance is emulated in parallel with the PV generator, making it possible to remove the RHP pole [3]. In this paper, a simpler solution is proposed to achieve the same objective. It is based on modifying the inner active current loop by using a Proportional (P) instead of a Proportional-Integral (PI) controller. If tuned following the guidelines provided in this paper, the P controller makes it possible to remove the RHP pole and thus to readily design a stable voltage control.

II. MODELING AND CONTROL OF THE INVERTER

A. Open-Loop Representation

The system is modeled in dq-reference frame after applying the power-invariant abc-dq transformation. Given that the RHP pole appears at low frequency, an ideal strong grid, i.e. \( L_g \approx 0 \), is assumed to avoid unnecessarily complicating the model. The average model is thus obtained as [5]

\[
\begin{align*}
L \frac{d i_d}{dt} &= d_d \cdot v_g + \alpha_i L \cdot i_q - r_i \cdot i_d - v_{gd} \\
L \frac{d i_q}{dt} &= d_q \cdot v_g - \alpha_i L \cdot i_q - r_i \cdot i_q - v_{gq} \\
C \frac{d v_{dc}}{dt} &= i_{pv} - d_q \cdot i_d - d_q \cdot i_q
\end{align*}
\]

where \( i_d \) and \( i_q \) are the inductor currents, \( v_{gd} \) and \( v_{gq} \) the grid voltages, \( d_d \) and \( d_q \) the duty cycles, \( r_i \) the equivalent resistance in series with the inductors, and \( \omega_0 \) the grid angular frequency.

The steady-state operating point can be solved from (1) by setting the derivate terms to zero. Taking into account that the Phase-Locked Loop (PLL) synchronizes the d-axis with the grid voltage, one obtains

\[
\begin{align*}
D_d &= \frac{V_g - \alpha_i L \cdot i_q + r_i \cdot i_d}{V_{dc}} \\
D_q &= \frac{\alpha_i L \cdot i_q + r_i \cdot i_q}{V_{dc}} \\
I_d &= \frac{V_{dc} \cdot I_{pv}}{V_{gd}}
\end{align*}
\]

The model shown in (1) is linearized by applying small-signal variations (marked with a circumflex) around the steady-state operating point represented in (2). The PV current depends on the PV voltage through a nonlinear I-V curve and its linearization can be expressed as \( i_{pv} = -g_{pv} \cdot \hat{u}_{pv} \), where \( g_{pv} \) is the PV dynamic conductance, equal to the absolute value of

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the slope of the I-V curve. Then, the small-signal model is expressed in state-space matrix representation, where the output variables are selected equal to the state variables, i.e. $Y = X$. The state equation can be defined as

$$\begin{bmatrix}
\frac{d\hat{i}_d}{dt} \\
\frac{d\hat{i}_q}{dt} \\
\frac{d\hat{v}_{dc}}{dt}
\end{bmatrix} =
\begin{bmatrix}
\frac{r_i}{L} & \alpha_b & \frac{D_d}{L} \\
-\alpha_b & \frac{r_i}{L} & \frac{D_q}{L} \\
-D_L & \frac{D_d}{C_{dc}} & \frac{D_q}{C_{dc}} - \frac{g_{pv}}{C_{dc}}
\end{bmatrix}
\begin{bmatrix}
\hat{i}_d \\
\hat{i}_q \\
\hat{v}_{dc}
\end{bmatrix}
+\begin{bmatrix}
\frac{V_d}{L} & 0 & 0 \\
0 & \frac{V_{dc,f}}{L} & 0 \\
\frac{-I_d}{C_{dc}} & -\frac{I_q}{C_{dc}} & 0
\end{bmatrix}
\begin{bmatrix}
\hat{d}_d \\
\hat{d}_q \\
\hat{v}_{gd}
\end{bmatrix}
+\begin{bmatrix}
0 & 1 & 0 \\
0 & 0 & 1 \\
-\frac{1}{L} & 0 & 0
\end{bmatrix}
\begin{bmatrix}
\hat{d}_d \\
\hat{d}_q \\
\hat{v}_{pd}
\end{bmatrix}
$$

From (3) and applying the Laplace transformation, the small-signal control-to-output transfer functions can be obtained. Omitting the grid disturbances, $v_{pd}$ and $v_{pq}$, the corresponding relationships are shown in (4), and the six transfer functions can be consulted in the appendix.

$$\begin{align*}
\hat{i}_d &= G_{\hat{i}d}(s) \cdot \hat{d}_d + G_{i\hat{i}d}(s) \cdot \hat{d}_q \\
\hat{i}_q &= G_{\hat{i}q}(s) \cdot \hat{d}_d + G_{i\hat{i}q}(s) \cdot \hat{d}_q \\
\hat{v}_{dc} &= G_{\hat{v}dc}(s) \cdot \hat{d}_d + G_{v\hat{v}dc}(s) \cdot \hat{d}_q
\end{align*}
$$

**B. Initial design of the Current Controllers**

As can be observed in Fig. 1, both currents $i_d$ and $i_q$ are regulated. The d-axis reference is obtained by the outer loop so that the PV voltage is controlled. The corresponding controller $C_{id}$ can thus be a P controller, since a dc current error will be compensated by the voltage controller $C_v$. In contrast, the q-axis current loop must track the reference with no dc error so that the reactive power is correctly controlled. As a result, the corresponding controller $C_{iq}$ should include an integral term, and thus a PI controller is selected. These considerations lead to the following current controllers:

$$C_{id}(s) = K_{pd}, \quad C_{iq}(s) = K_{pq} \cdot \frac{T_n s + 1}{T_n s}, \quad (5)$$

where $K_{pd}$ and $K_{pq}$ are the proportional gains and $T_n$ is the time constant of the PI controller.

The proportional gains will be tuned by using frequency-domain analysis. Although the system is actually Multiple-Input Multiple-Output (MIMO), it can be treated as two Single-Input Single-Output (SISO) for high frequencies around the crossover frequency. This leads to the current control loops shown in Fig. 2, where $i_d^*$ and $i_q^*$ are the reference currents, $d_d^*$ and $d_q^*$ the reference duty cycles, $i_{d,f}$ and $i_{q,f}$ the measured currents, $D_i$ represents the computation and zero-order hold delays and $H_i$ the current filters.

From Fig. 2, the compensated open-loop transfer functions for both d- and q-axes can be expressed as

$$\begin{align*}
OL_d(s) &= C_{id} \cdot D_i \cdot G_{dd} \cdot H_i \\
OL_q(s) &= C_{iq} \cdot D_i \cdot G_{qq} \cdot H_i
\end{align*} \quad (6)$$

**Fig. 1.** Three-phase photovoltaic inverter connected to the grid and its control scheme in dq reference frame.

**Fig. 2.** D- and q-axes current control loops.
The plant transfer functions $G_{dl}(s)$ and $G_{dq}(s)$, without parasitics and for $I_p = 0$, are shown in (7) and (8), where $g_r = g_{pv} - g_0$ is the PV total conductance and $g_0 = I_p/v_d$ is the PV static conductance. It is worth highlighting that, while $g_{pv}$ and $g_0$ are always positive, the total conductance $g_r$ is positive when $v_d$ is over the MPP voltage $v_{mpp}$ and negative for $v_d < v_{mpp}$. For the controller design, the high frequency approximation shown in (9) can be applied.

\[
G_{dl}(s) = \frac{V_d s}{L_{dc} s^3 + L g_p s^2 + \left(\frac{V_d}{v_d} + L_{dc} \omega_0 \right) s + L g_p \omega_0^2} 
\]

\[
G_{dq}(s) = \frac{V_d s}{L_{dc} s^3 + L g_p s^2 + \left(\frac{V_d}{v_d} + L_{dc} \omega_0 \right) s + L g_p \omega_0^2} + \frac{V_d^2}{L V_p} \]

\[
G_{dl}(s) = G_{dq}(s) = \frac{V_d}{L s} \]

Furthermore, if the time constant of the q-axis controller is selected so that $T_c \omega_{cqq} \geq 15$, where $\omega_{cqq}$ is the q current crossover frequency, the controller $C_q$ resembles a P controller around $\omega_{cqq}$. Taking into account these considerations, the open-loop transfer functions shown in (6) can be approximated as

\[
\begin{align*}
OL_{d,\text{approx}}(s) &= K_{pd} e^{-\frac{s}{T_i}} \cdot \frac{1 - e^{-\frac{s}{T_i}}}{s} \cdot \frac{V_d}{L s} \cdot \frac{1}{s/\omega_{cqd} + 1} \\
OL_{q,\text{approx}}(s) &= K_{pq} e^{-\frac{s}{T_i}} \cdot \frac{1 - e^{-\frac{s}{T_i}}}{s} \cdot \frac{V_d}{L s} \cdot \frac{1}{s/\omega_{cqq} + 1}
\end{align*}
\]

where $T_i$ is the sampling time, and $\omega_{cqd}$ the current filter cutoff frequency.

Parameters $K_{pd}$ and $K_{pq}$ are designed to have certain crossover frequencies $\omega_{cqd}$ and $\omega_{cqq}$, respectively. Furthermore, the selected operating point is with $v_d = v_{dc,max}$, since this point represents the worst conditions concerning stability. Hence, from (10), the proportional gains can be selected as

\[
\begin{align*}
K_{pd} &= \frac{L \cdot \omega_{cqd}}{V_{dc,max} \cdot S_{inv}} = \frac{x_L \cdot V_d^2}{V_{dc,max} \cdot S_{inv}} \cdot \frac{\omega_{cqd}}{\omega_0}, \\
K_{pq} &= \frac{L \cdot \omega_{cqd}}{V_{dc,max} \cdot S_{inv}} = \frac{x_L \cdot V_d^2}{V_{dc,max} \cdot S_{inv}} \cdot \frac{\omega_{cqq}}{\omega_0}
\end{align*}
\]

where $x_L$ is the per-unit impedance of the inductor and $S_{inv}$ is the inverter nominal power.

Once the crossover frequencies are chosen, the phase margins $PM_{dl}$ and $PM_{dq}$ can be obtained from (10) as

\[
\begin{align*}
PM_{dl} &= \frac{\pi}{2} - \frac{3}{2} T_i \omega_{cqd} - \arctan \left( \frac{\omega_{cqd}}{\omega_{cqd}} \right) \\
PM_{dq} &= \frac{\pi}{2} - \frac{3}{2} T_i \omega_{cqq} - \arctan \left( \frac{\omega_{cqq}}{\omega_{cqq}} \right)
\end{align*}
\]

For the selection of the crossover frequencies, two restrictions must be taken into account. The first one can be derived from (12) and shows that their maximum value is limited so that a certain phase margin is ensured. The second one is related to the low-frequency RHP pole which appears when operating with $v_d < v_{mpp}$. This condition is only applied to the $d$ axis and will be described below.

As an example, the current control design is carried out for a system with the parameters shown in Table I, where $f_{sw}$ is the switching frequency and $f_s$ is the sampling frequency. In order to have a phase margin $PM_{dl} = PM_{dq} = 2\pi/9$ ($40^\circ$), from (12) both crossover frequencies are selected as $f_{cqd} = f_{cqq} = 2000$ Hz. Then, from (11), the proportional gains are obtained as $K_{pd} = K_{pq} = 10.24 \cdot 10^{-3}$ A$^{-1}$. Concerning the time constant $T_i$ for the q-axis controller, it is selected so that $T_i \omega_{cqq} = 20$ and thus the phase margin is hardly affected.

<table>
<thead>
<tr>
<th>TABLE I. SYSTEM PARAMETERS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rated values</td>
</tr>
<tr>
<td>$S_{inv} = 50$ kVA, $f_s = 50$ Hz, $V_s = 400$ V</td>
</tr>
<tr>
<td>DC voltage</td>
</tr>
<tr>
<td>$V_{dc,max} = 570$ V, $V_{dc,max} = 1000$ V</td>
</tr>
<tr>
<td>Input capacitor</td>
</tr>
<tr>
<td>$C_{aprox} = 622 \mu F$</td>
</tr>
<tr>
<td>Inverter inductor</td>
</tr>
<tr>
<td>$x_L = 0.08, L = 815 \mu H, r_L = 45 m\Omega$</td>
</tr>
<tr>
<td>PV generator</td>
</tr>
<tr>
<td>$G_{aprox} = 150 mS, G_{aprox} = 1500 mS$</td>
</tr>
<tr>
<td>Inverter frequencies</td>
</tr>
<tr>
<td>$f_{sw} = 15$ kHz, $f_s = 30$ kHz, $f_{cqd} = 8$ kHz</td>
</tr>
<tr>
<td>Control design</td>
</tr>
<tr>
<td>$PM_{dl} = PM_{dq} = 40^\circ, f_{cqd} = f_{cqq} = 2000$ Hz</td>
</tr>
<tr>
<td>$K_{pd} = K_{pq} = 10.24 \cdot 10^{-3}$ A$^{-1}$</td>
</tr>
<tr>
<td>$T_i \omega_{cqd} = 20 \rightarrow T_i = 1.59$ ms</td>
</tr>
</tbody>
</table>

Figure 3 shows the Bode plot of the compensated open-loop transfer functions for $v_d = v_{dc,max}$. The three represented curves are the complete open-loop transfer function for the $d$ axis, $OL_d = C_{aprox} D_L G_{dl} H_{aprox}$, the complete open-loop transfer function for the $q$ axis, $OL_q = C_{aprox} D_L G_{dq} H_{aprox}$, and the open-loop transfer function $OL = OL_{d,\text{approx}} + OL_{q,\text{approx}}$ approximated as shown in (10). As can be observed, the three plots are almost identical at high frequencies and as a result the crossover frequencies and phase margins are close to their target values, which justifies the use of (10)–(12) for the controller design.

![Bode Diagram](image_url)

FIG. 3. Compensated open-loop transfer functions for $v_d = v_{dc,max}$.

Finally, once the current controls have been implemented, closed-loop transfer functions $G_{cl}$ can be defined so that
expression, where Figure 5(a) employs the currents
as intermediate variables and will be used for the ideal case, and
Figure 5(b) employs the duty cycles as intermediate variables and will be used for the real case.

\[
\begin{align*}
\dot{i}_d &= G_{cl,dd}(s) \cdot \dot{v}_{dc}^* + G_{cl,dq}(s) \cdot \dot{i}_q^* \\
\dot{i}_q &= G_{cl,dq}(s) \cdot \dot{v}_{dc}^* + G_{cl,qq}(s) \cdot \dot{i}_q^* \\
\dot{v}_{dc} &= G_{cl,dd}(s) \cdot \dot{i}_d + G_{cl,dq}(s) \cdot \dot{i}_q
\end{align*}
\] (13)

C. Open-Loop Transfer Function of the Voltage Regulation

The voltage control loop is shown in Fig. 4, where \( v_{dc}^* \) is the reference PV voltage, \( v_{dc,f} \) the measured voltage, \( C_v \) represents the PI controller, \( D \), the computation and zero-order hold delays and \( H_v \) the voltage filter.

\[ G_{cl,vd}(s) = \frac{V_{dc}}{s^2 + \frac{1}{C_v} + \frac{R_v}{C_v}} \]

Fig. 4. PV voltage control loop.

To design the voltage controller, a model of the transfer function \( G_{cl,vd} \) valid at low frequencies, must be first obtained. For this purpose, processes which occur at high frequencies, such as the computation and zero-order hold delays \( D \), and the current filter \( H_v \), can be disregarded. Figure 5 shows two equivalent schemes which make it possible to obtain \( G_{cl,vd} \) expression, where Figure 5(a) employs the currents \( i_d \) and \( i_q \) as intermediate variables and will be used for the ideal case, and Figure 5(b) employs the duty cycles \( d_d \) and \( d_q \) as intermediate variables and will be used for the real case.

\[ C_{eq} = C_{dc} + \frac{I_r \cdot g_{pv}}{V_{dc}} \cdot \frac{1}{K_{pd}}, \quad g_{eq} = g_t + \frac{V_g^2}{2V_{dc}} \cdot \frac{1}{K_{pd}} \] (18)

D. Final Design of the Current Controllers

Concerning stability, the most critical operating point is with \( V_{dc} = V_{dc,min} \), where the total conductance is minimum, \( g_t = g_{t,min} \), and \( g_{eq} \approx 0 \). At this point, from (18) and considering \( V_{dc,min} \approx \sqrt{2} V_g \) one obtains

\[ C_{eq} = C_{dc}, \quad g_{eq,min} = g_{t,min} + \frac{1}{2V_{dc,min}} \cdot \frac{1}{K_{pd}} \] (19)

The minimum total conductance can be estimated as [3].

From Fig. 5(a), the transfer function \( G_{cl,vd} \) can be obtained as shown in (14). If a fast PI controller is used in both d and q axes, the current loop can be taken as ideal at low-frequencies, that is \( G_{cl,dd} = 1 \) and \( G_{cl,dq} = 0 \), leading to the approximation expressed in (15). As can be observed, in this case the closed-loop transfer function presents a pole at \( s = -g_t C_{dc} \), which is at the RHP when \( g_t \) is negative, i.e. when the voltage is below the MPP voltage and the slope of the P-V curve is positive. In order to reduce this pole frequency and allow for a stable voltage control input, the control capacitor must be oversized [5].

\[ G_{cl,vd}(s) = \frac{V_{dc}}{s^2 + \frac{1}{C_v} + \frac{R_v}{C_v}} \]

\[ G_{cl,vd}(s) = \frac{V_{dc}}{s^2 + \frac{1}{C_v} + \frac{R_v}{C_v}} \]

However, when a P controller is used as regulator \( C_{dc} \), the ideal current loop assumption, usually made in the literature, is no longer valid. Furthermore, the model must consider a MIMO system. In so doing, from Fig. 5(b), transfer function \( G_{cl,vd} \) can be expressed as shown in (16). This expression can be approximated as the simple first-order transfer function shown in (17) and (18). This approximation is derived in the appendix and reproduces accurately the closed-loop low-frequency behaviour.

As can be observed, (17) is very similar to (15), except that using a P controller in the d-axis transforms the real capacitance \( C_{dc} \) and the real conductance \( g_t \) into an equivalent capacitance \( C_{eq} \) and an equivalent conductance \( g_{eq} \). In practice, the effect of the current closed-loop is a slight modification of the dc capacitance but an important increase of the total conductance. Since the closed-loop pole is now located at \( s = -g_{eq} C_{eq} \) instead of \( s = -g_t C_{dc} \), the frequency of the pole is reduced, thus moving towards the LHP for \( g_t < 0 \). It is worth noting that the values of \( C_{eq} \) and \( g_{eq} \) are affected by the d-axis controller \( C_{dc} \) but are totally independent of the q-axis controller \( C_{qq} \).

\[ G_{cl,vd}(s) = \frac{C_v G_{dd} + C_q G_{dq} - G_{dq} G_{dd}}{1 + C_v G_{dd} + C_q G_{dq} + C_q G_{dq} - G_{dq} G_{dd}} \] (16)

\[ G_{cl,vd}(s) = \frac{-V_g V_{dc}}{C_{eq} s + g_{eq}} \] (17)

\[ C_{eq} = C_{dc} + \frac{I_r \cdot g_{pv}}{V_{dc}} \cdot \frac{1}{K_{pd}}, \quad g_{eq} = g_t + \frac{V_g^2}{2V_{dc}} \cdot \frac{1}{K_{pd}} \] (18)

Fig. 5. Schemes used to obtain the closed-loop transfer function \( G_{cl,vd} \).
\[ g_{\text{c}, \text{min}} = \frac{S_v}{V_{\text{dc}, \text{min}}^2} \]  \hspace{1cm} (20)

Then, substituting \(K_Pd\) and \(g_{\text{c}, \text{min}}\) expressions, (11) and (20), into (19) leads to another expression for the minimum value of \(g_{\text{eq}}\), that is

\[ g_{\text{eq}, \text{min}} = \frac{S_v}{V_{\text{dc}, \text{min}}^2} \left( \frac{V_{\text{dc}, \text{max}}}{x_c \omega_{c, \text{eq}}^2} - 1 \right) \]  \hspace{1cm} (21)

In a similar way, if the input capacitance is defined as a function of its per-unit impedance, \(x_c\), it can be expressed as

\[ C_{\text{eq}} = C_{\text{dc}} = \frac{1}{x_c} \frac{S_v}{V_g} = 2 \frac{S_v}{x_c a_b} V_{\text{dc}, \text{min}}^2 \]  \hspace{1cm} (22)

Thus, from (21) and (22), the maximum frequency of the closed-loop pole can be obtained as

\[ \omega_{p, \text{max}} = -\frac{g_{\text{eq}, \text{min}}}{C_{\text{eq}}} = \frac{x_c a_b}{2} \left( 1 - \frac{V_{\text{dc}, \text{max}}}{V_{\text{dc}, \text{min}}} \right) \frac{x_c \omega_{c, \text{eq}}}{x_c a_b} \]  \hspace{1cm} (23)

As can be observed, the frequency of this pole depends on the crossover frequency selected for the d current control. In this paper, it is first proposed to restrict its value so that \(\omega_{p, \text{max}} = 0\) and thus the RHP pole is avoided in the whole operation range. From (23), one obtains

\[ \omega_{c,d} = \frac{V_{\text{dc}, \text{max}}}{V_{\text{dc}, \text{min}}} \frac{a_b}{x_c} \Rightarrow g_{\text{eq}, \text{min}} = 0, \quad \omega_{p, \text{max}} = 0 \]  \hspace{1cm} (24)

As a result, considering \(V_{\text{dc}, \text{min}} \approx \sqrt{2} V_g\) (17) and (24), the transfer function \(G_{c,d}\) then becomes

\[ G_{c,d}(s) = \frac{1}{\sqrt{2}} C_{\text{dc}} \cdot s \]  \hspace{1cm} (25)

An alternative is to choose a higher \(\omega_{c,d}\), which makes that the current closed-loop becomes unstable in the worst operating point, but limiting the frequency of the pole to \(\omega_{c,d}/2\), where \(\omega_{c,d}\) is the crossover frequency of the PV voltage control. In this way, the outer loop will be able to stabilize the control. From (23), the crossover frequency should then be selected as

\[ \omega_{c,d} = \frac{V_{\text{dc}, \text{max}}}{V_{\text{dc}, \text{min}}} \frac{a_b}{x_c} \frac{1}{x_c a_b} \Rightarrow \omega_{p, \text{max}} = \frac{\omega_{c,d}}{2} \]  \hspace{1cm} (26)

Figure 6 shows the frequency of the dominant pole at \(V_{\text{dc}} = V_{\text{dc}, \text{min}}\), as a function of the crossover frequency \(f_{\text{cid}}\), for the system parameters presented in Table I. As can be observed, the pole frequency obtained with the first-order approximation, as expressed in (23), is very similar to the exact value, obtained with a complete MIMO model which takes into account all dynamics. If the crossover frequency is selected equal to 2000 Hz, so that the phase margin is 40º (see section II.B), the dominant pole is located at +18.1 Hz. As a result, it is not possible to stabilize the system without increasing the input capacitance. However, if the crossover frequency is selected equal to 1100 Hz following (24), the dominant pole is located at the origin. Finally, choosing the crossover frequency equal to 1375 Hz according to (26), the dominant pole is located at \(f_{\text{cid}}/2 = \pm 7.5\) Hz. The two latter situations make it possible to design a stable outer loop without requiring an increase of the input capacitance.

### III. SIMULATION RESULTS

The proposed method is validated with the PSIM software for the system with parameters shown in Table I. The voltage control is tested for three different d-axis current controllers: (P1) for a P controller designed as shown in section II.B, i.e. to have maximum crossover frequency, \(f_{\text{cid}} = 2000\) Hz; (P2) for a P controller designed as proposed in this paper to avoid the RHP pole, i.e. with \(f_{\text{cid}} = 1100\) Hz from (24); and (PI) for a PI controller designed to have \(f_{\text{cid}} = 1100\) Hz and phase margin \(PM_{\text{Pi}} = 40º\). These specifications as well as the ones applied to design the voltage controller are included in Table II.

<table>
<thead>
<tr>
<th>TABLE II. SYSTEM PARAMETERS</th>
</tr>
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<tbody>
<tr>
<td>(P) controller for maximum (f_{\text{cid}})</td>
</tr>
<tr>
<td>(K_{Pd} = 10.24 \pm 10^{-3}) A⁻¹, (PM_{i} = 62.2º)</td>
</tr>
<tr>
<td>(P) controller to avoid the RHP pole</td>
</tr>
<tr>
<td>(K_{Pd} = 5.63 \pm 10^{-3}) A⁻¹, (PM_{i} = 62.2º)</td>
</tr>
<tr>
<td>(PI) controller</td>
</tr>
<tr>
<td>(K_{Pd} = 5.21 \pm 10^{-3}) A⁻¹, (T_{\text{nd}} = 0.35) ms</td>
</tr>
<tr>
<td>Voltage loop</td>
</tr>
</tbody>
</table>

The results are shown in Fig. 7 for steps in the voltage reference near and below the MPP voltage \(V_{\text{MPP}} = 628\) V, where the following variables are represented: dc voltage, reference dc voltage, d-axis current and PV total conductance \(g_{\text{t}}\). As can be observed, the response for the PI controller becomes oscillating at low voltage [Fig. 7(a)], while the proposed P2 controller ensures stability in the whole operating range, including with a total conductance as low as \(g_{\text{t}} = -120\) mS [Fig. 7(b)]. Finally, the response for the PI controller becomes unstable below MPP voltage [Fig. 7(c)].
IV. CONCLUSION

This paper analyzes the voltage regulation in grid-connected three-phase PV inverter, paying attention to the effect of the inner current loop. When using a PI controller, at low-frequencies the current loop can be considered as ideal and the inverter as a constant power load, which exposes the system RHP pole and forces to increase the input capacitance. On the other hand, using in the d-axis a P controller with restricted bandwidth reduces the frequency of the RHP pole, permitting to obtain an adequate voltage regulation in the whole operating range with no need of oversizing the capacitance.

APPENDIX

This appendix explains how to obtain the first-order approximation shown in (17) on the basis of (16). First of all, the control-to-output transfer function, defined by (4), without parasitics and for $I_q = 0$, can be expressed as

$$G_{av}(s) = \frac{V_d s (C_{dc} s + g_v)}{d(s)} \quad (A.1)$$

$$G_{vy}(s) = \frac{V_a s^2 + V_d g_v s + \frac{1}{L} V_p^2}{d(s)} \quad (A.2)$$

$$G_{dv}(s) = \frac{V_d a_v (C_{dc} s + g_v)}{d(s)} \quad (A.3)$$

$$G_{qv}(s) = -\frac{V_q a_v}{d(s)} \quad (A.4)$$

$$G_{vd}(s) = -\frac{V_d s}{d(s)} \quad (A.5)$$

$$G_{qv}(s) = -\frac{V_q s}{d(s)} \quad (A.6)$$

$$d(s) = LC_{dc} s^3 + L g_v s^2 + \left(\frac{V_p^2}{V_d} + LC_{dc} a_v^2\right)s + L g_v a_v^2 \quad (A.7)$$

In addition, from (A.1)–(A.7), the following expressions can be obtained:

$$G_{ad} \cdot G_{dq} - G_{aq} \cdot G_{vd} = -\frac{V_p V_d}{L} \frac{1}{d(s)} \quad (A.8)$$

$$G_{dd} \cdot G_{dq} - G_{dq} \cdot G_{vd} = \frac{V_p^2}{L} \frac{C_{dc} s + g_v}{d(s)} \quad (A.9)$$

From (16), at low frequencies the term 1 in the denominator is much lower than the other term. As a result, it can be neglected, which results in

$$G_{av,ld}(s) = \frac{G_{av} + C_{av} (G_{av} G_{dq} - G_{aq} G_{vd})}{G_{av} + C_{av} (G_{av} G_{dq} - G_{aq} G_{vd})} \quad (A.10)$$
Substituting the corresponding expressions (5), (A.1), (A.2), (A.5), (A.8) and (A.9) into (A.10), and reorganizing, leads to

\[ G_{cl,vd}(s) = \frac{V_{pd}}{V_{dc}} \frac{a_2 s^2 + a_1 s + 1}{b_2 s^3 + b_1 s^2 + b_0 s + b_0}, \]  
(A.11)

where

\[
\begin{align*}
    a_2 &= \frac{L}{V_{dc}} \frac{T_s}{K_{pq}}, \quad a_1 = T_s \\
    b_2 &= \frac{L C_{de}}{V_{dc}} \frac{T_s}{K_{pq}} + \frac{L g_{pe}}{V_{dc}} \frac{T_s}{K_{pq}} + \frac{L C_{de}}{K_{pq} V_{dc}} + T_s C_{de} \\
    b_1 &= C_{de} + \frac{L g_{pe}}{V_{dc}} \frac{1}{K_{pq} V_{dc}} + \frac{V^2}{V_{dc} K_{pq}} + s g_t \\
    b_0 &= g_t + \frac{V^2}{V_{dc} K_{pq}}
\end{align*}
\]  
(A.12)

At low frequencies, \( G_{cl,vd}(s) \) is determined by one dominant pole. From (A.11) and (A.12), the Padé approximant of order \([0/1]\) can be obtained as

\[
\begin{align*}
    G_{cl,vd}(s) &= -\frac{V_s}{V_{dc}} \frac{1}{C_{eq} s + g_{so}} \\
    g_{so} &= b_0 = g_t + \frac{V^2}{V_{dc} K_{pq}} \\
    C_{eq} &= b_1 - b_0 \cdot a_1 = C_{de} + \frac{L \cdot g_{pe}}{V_{dc}} \cdot \frac{1}{K_{pq}}
\end{align*}
\]  
(A.13)

REFERENCES


